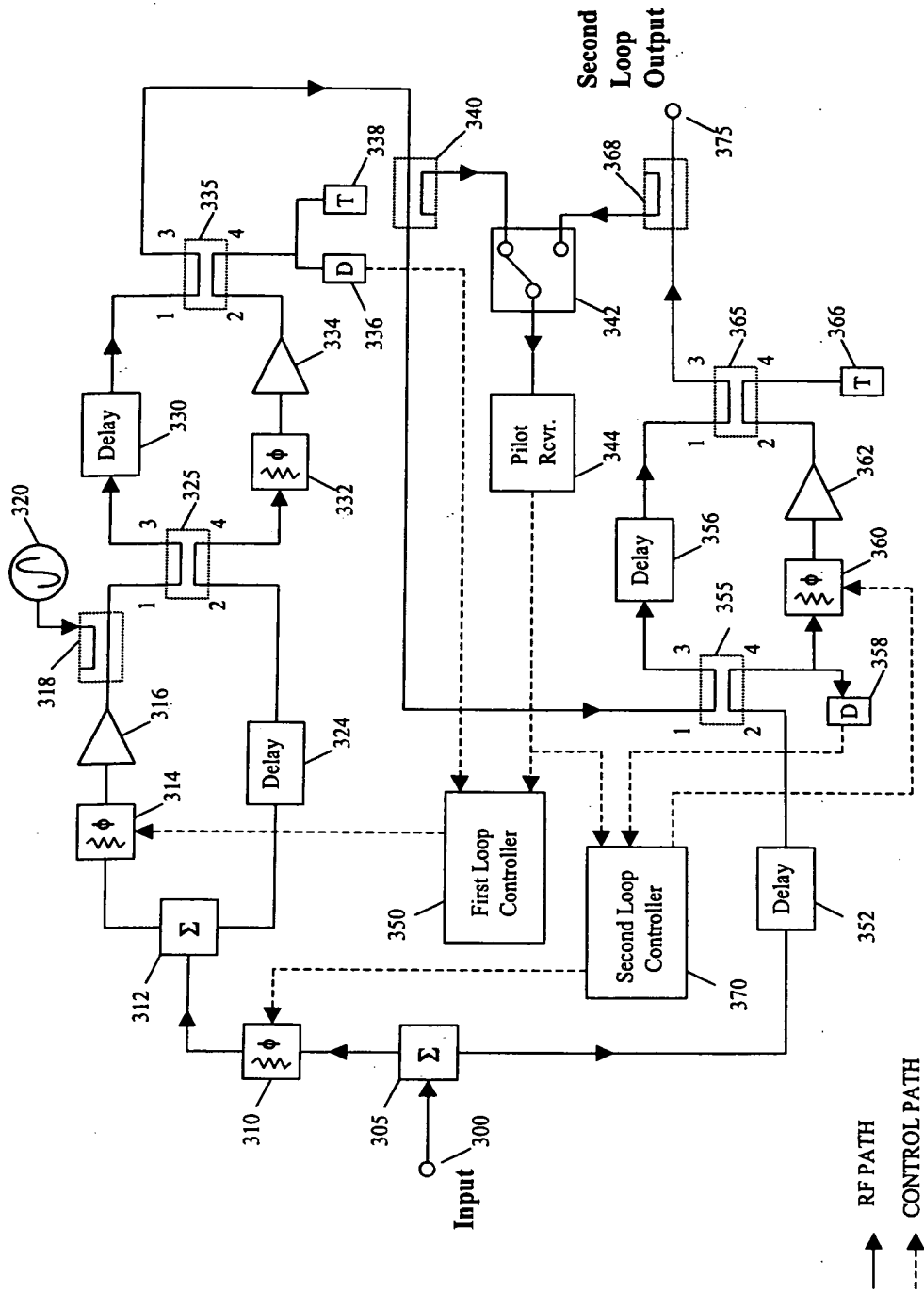


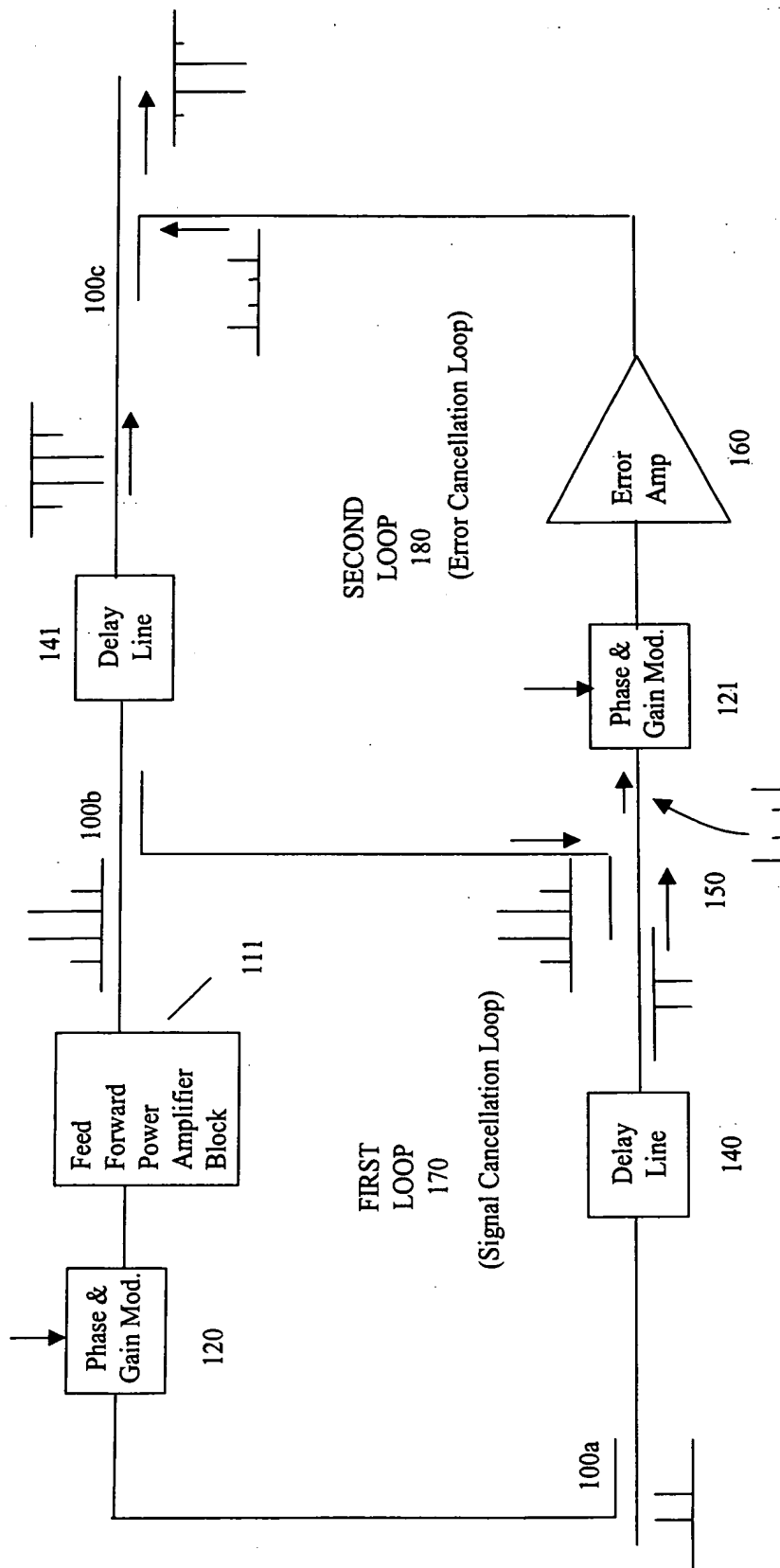
PRIOR ART

FIG. 1



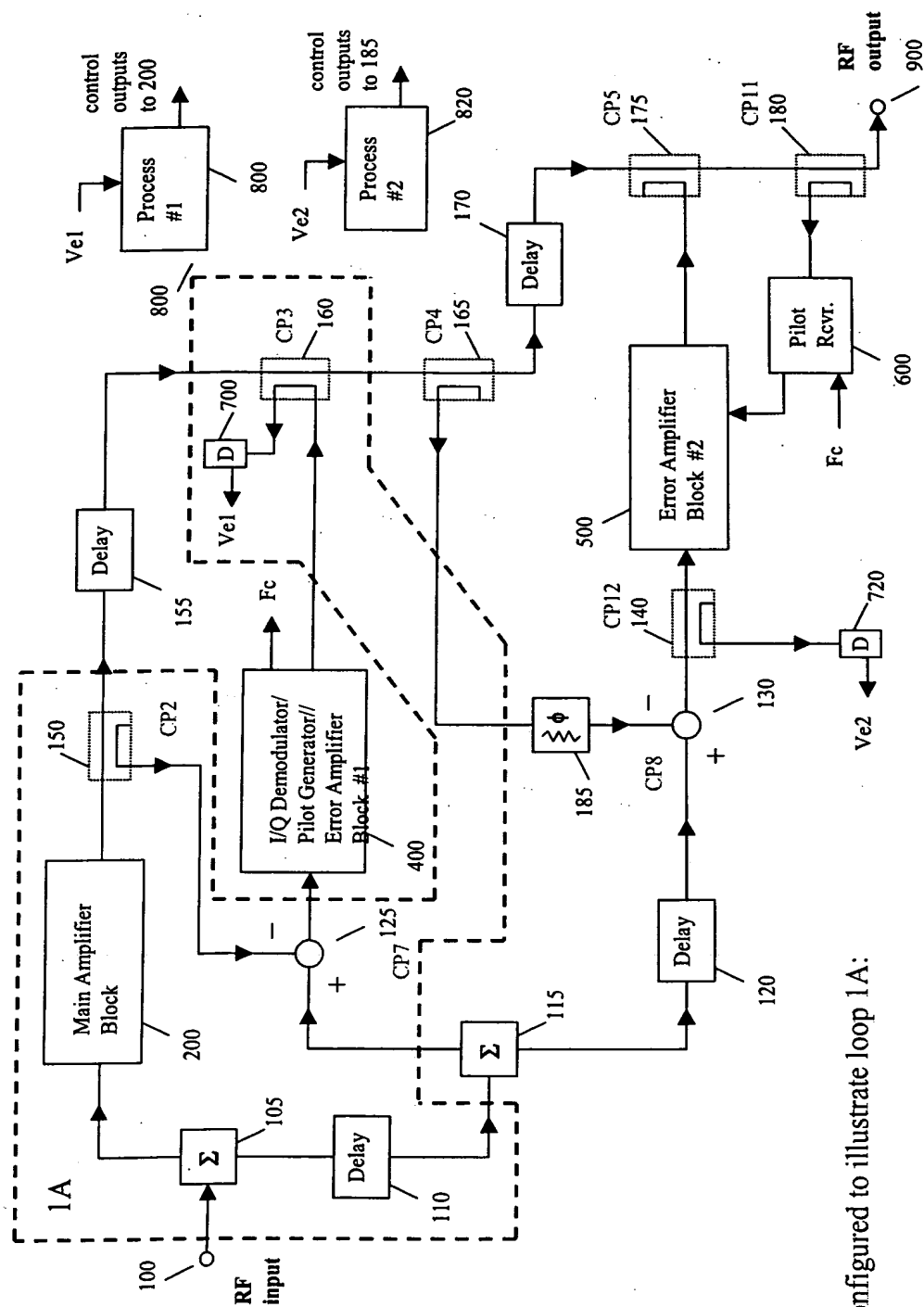
PRIOR ART

FIG. 2



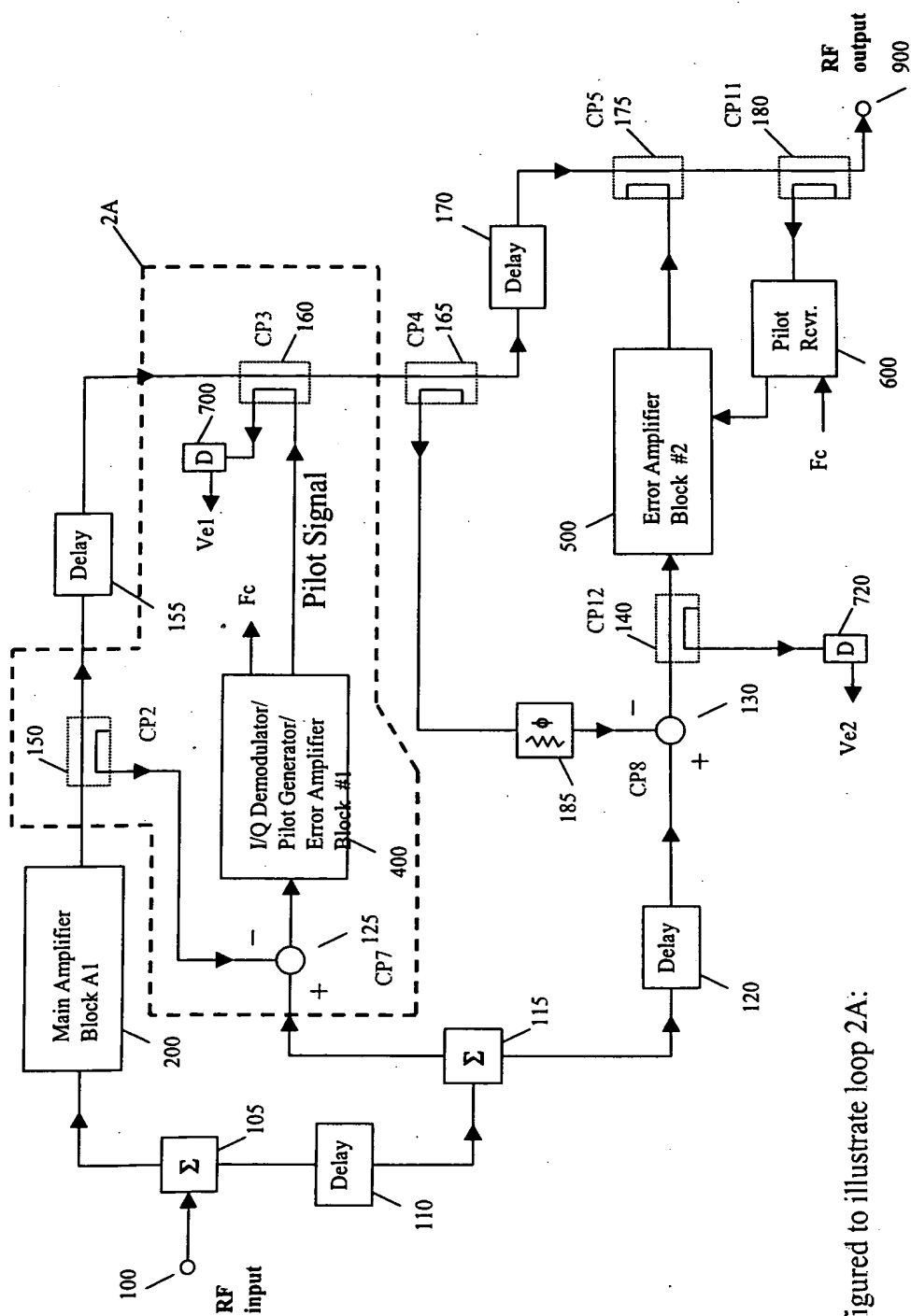
PRIOR ART

FIG. 3



configured to illustrate loop 1A:

FIG. 4



configured to illustrate loop 2A:

FIG. 5



FIG. 7

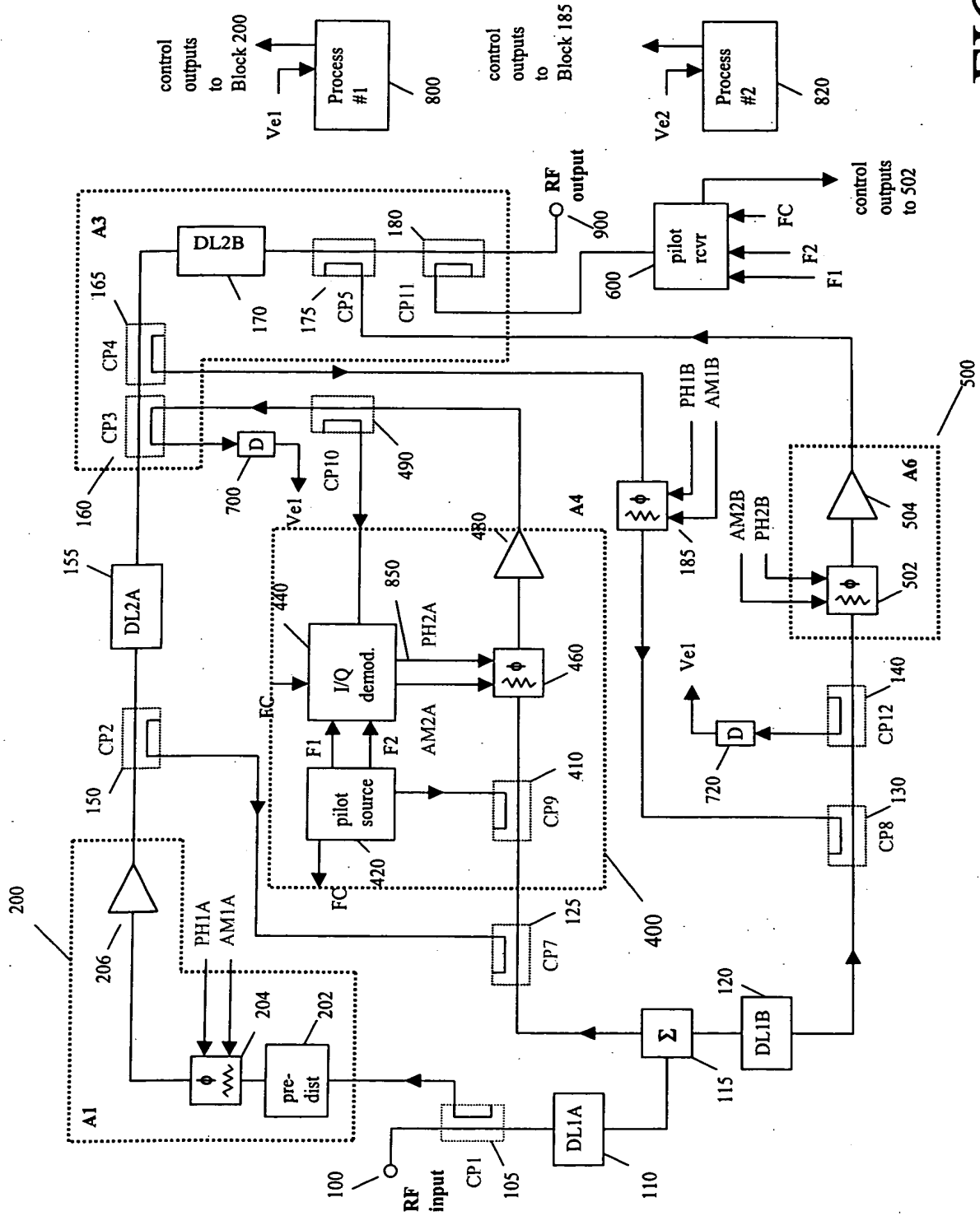


FIG. 8

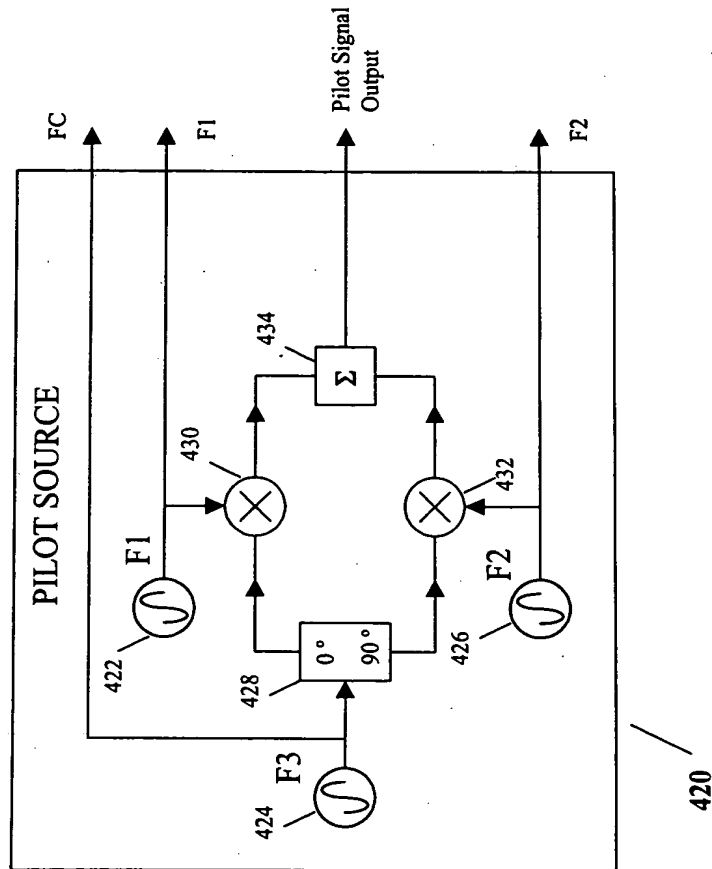


FIG. 9

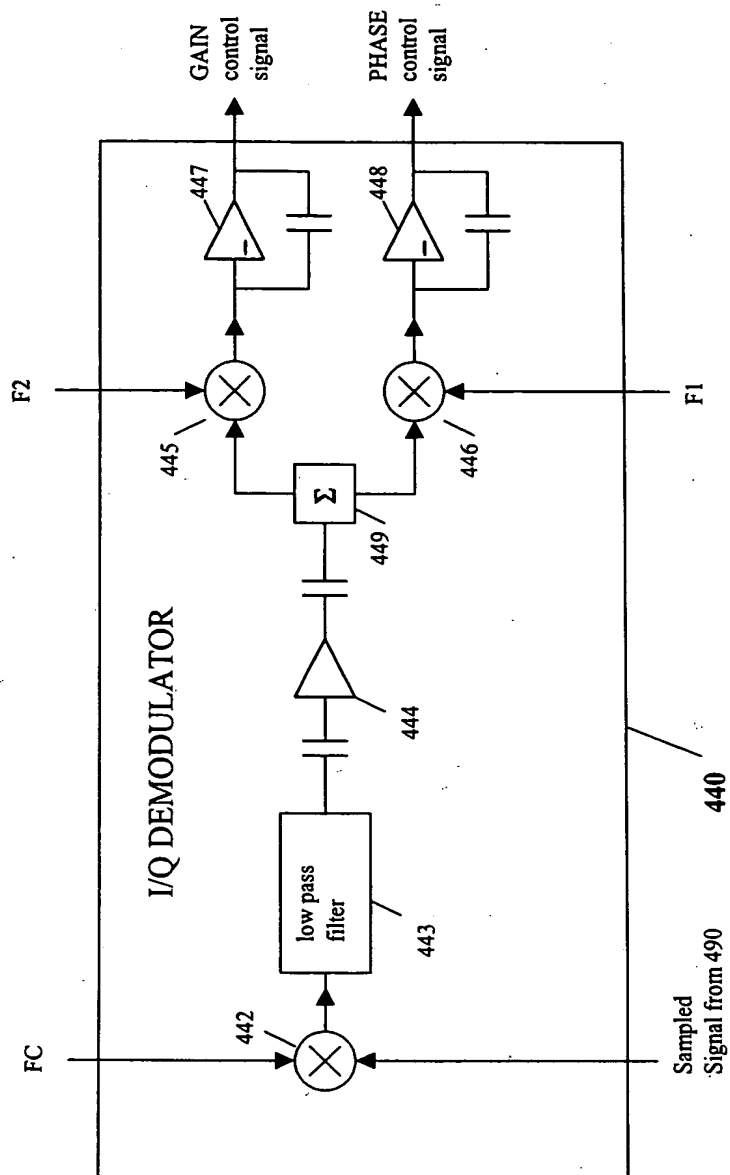
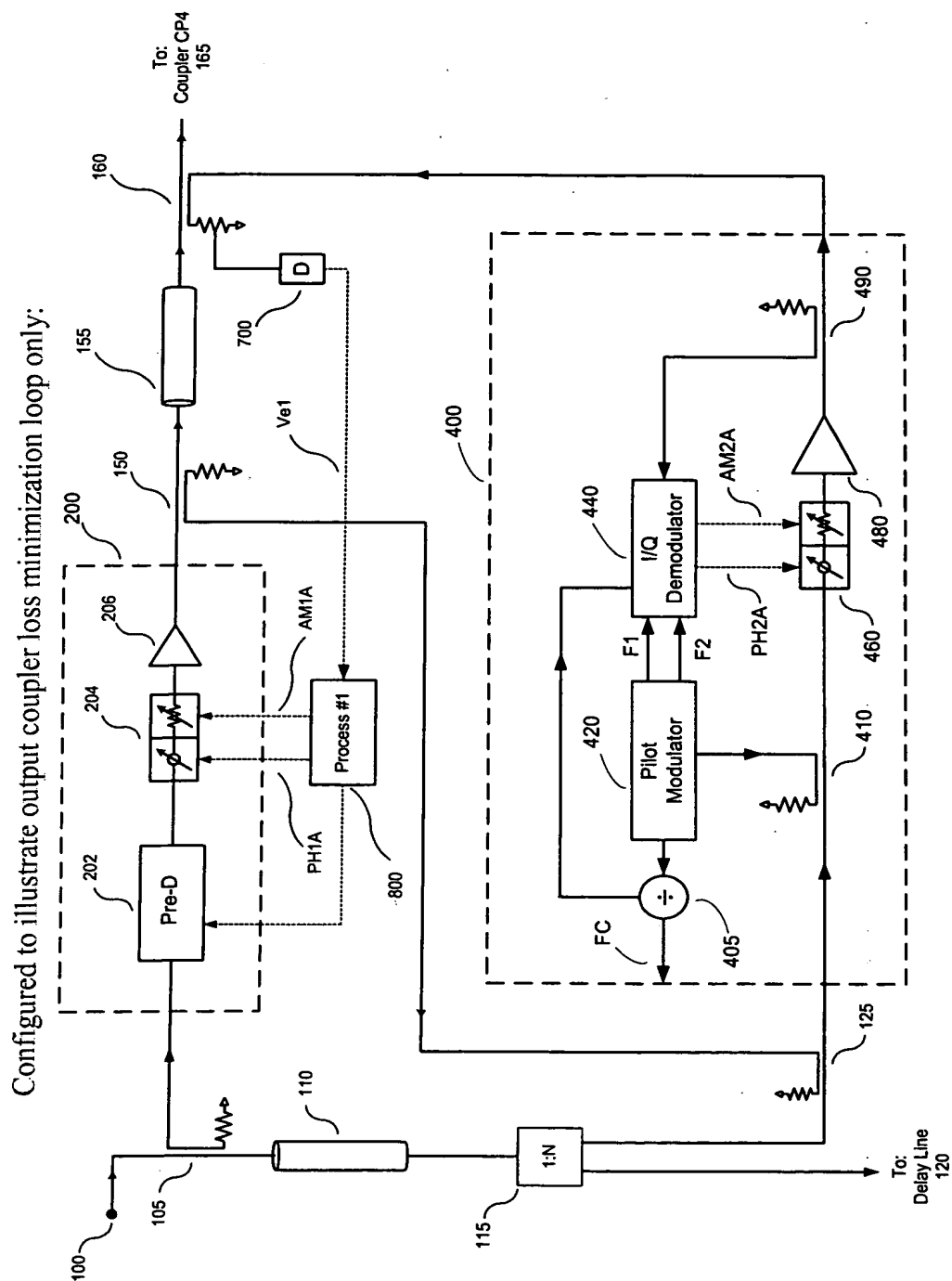


FIG. 10



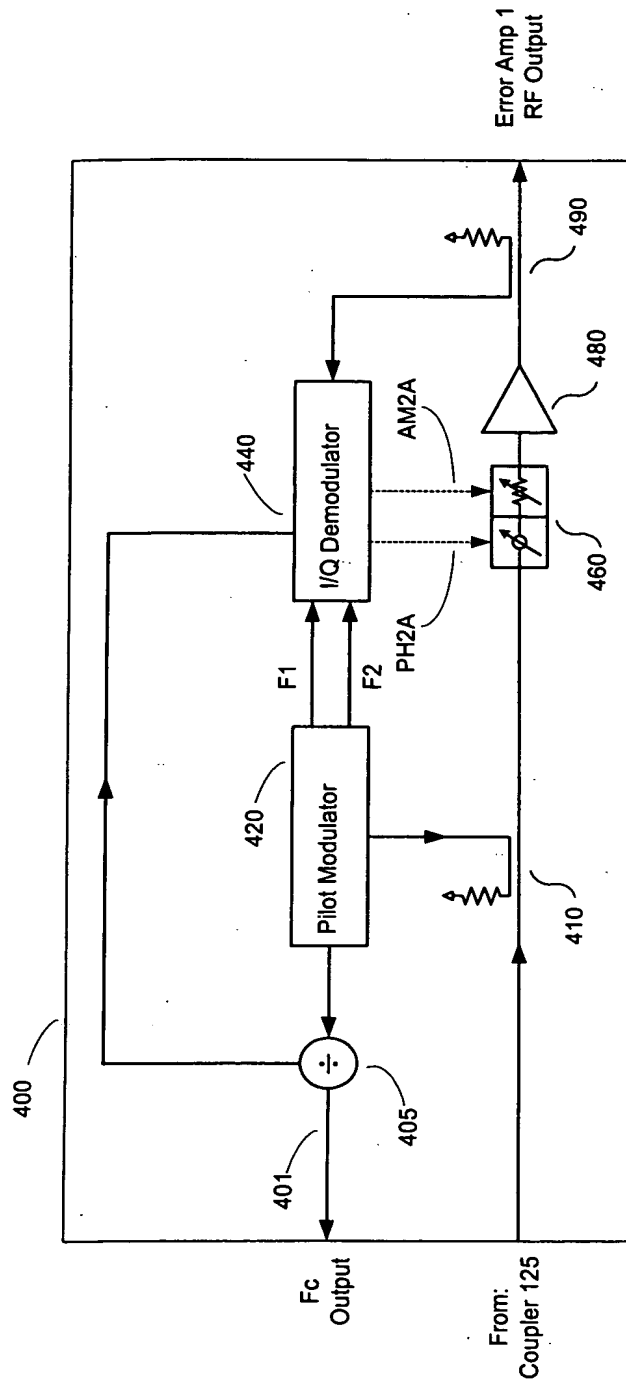


FIG. 12

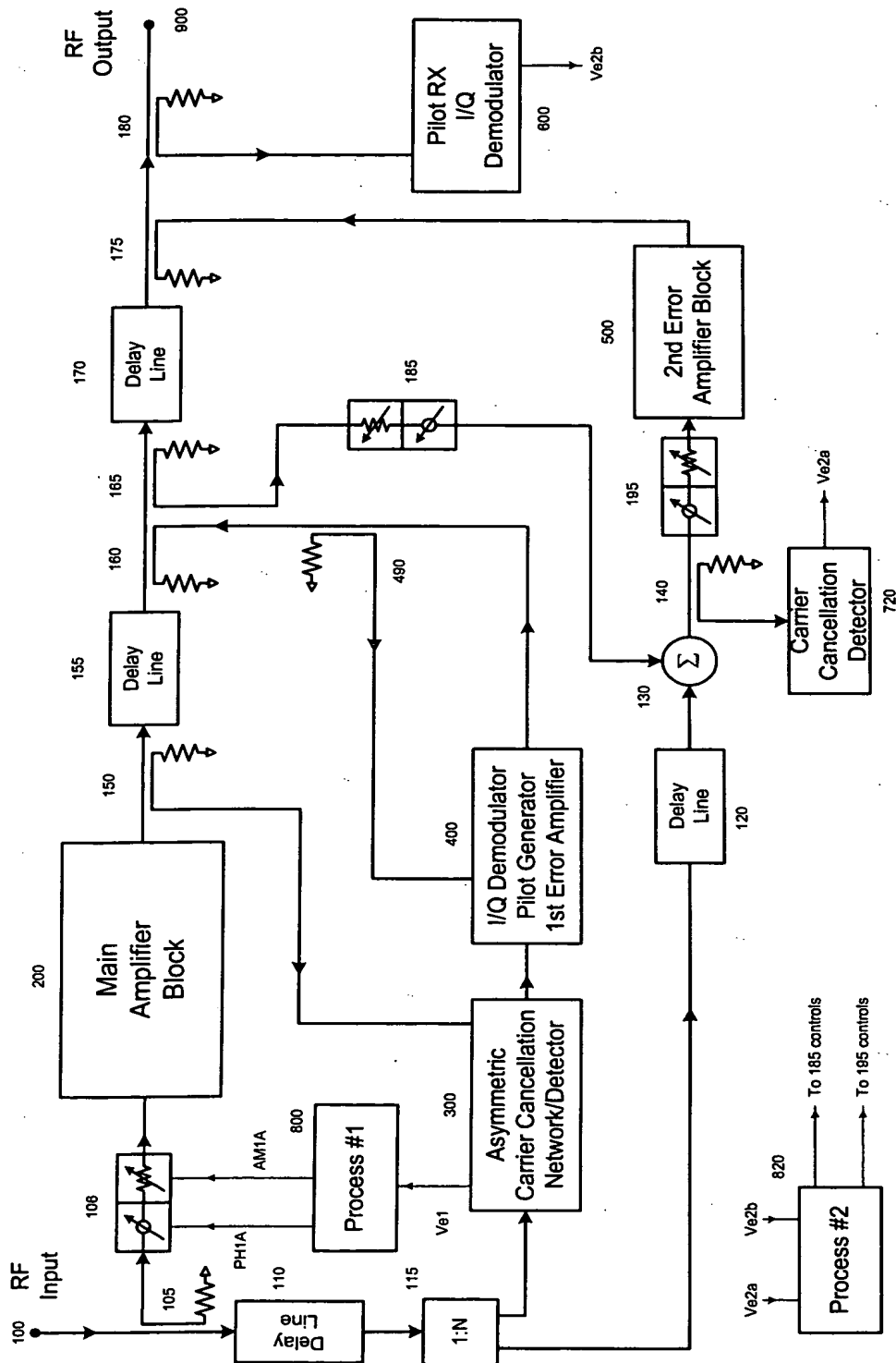


FIG. 13A

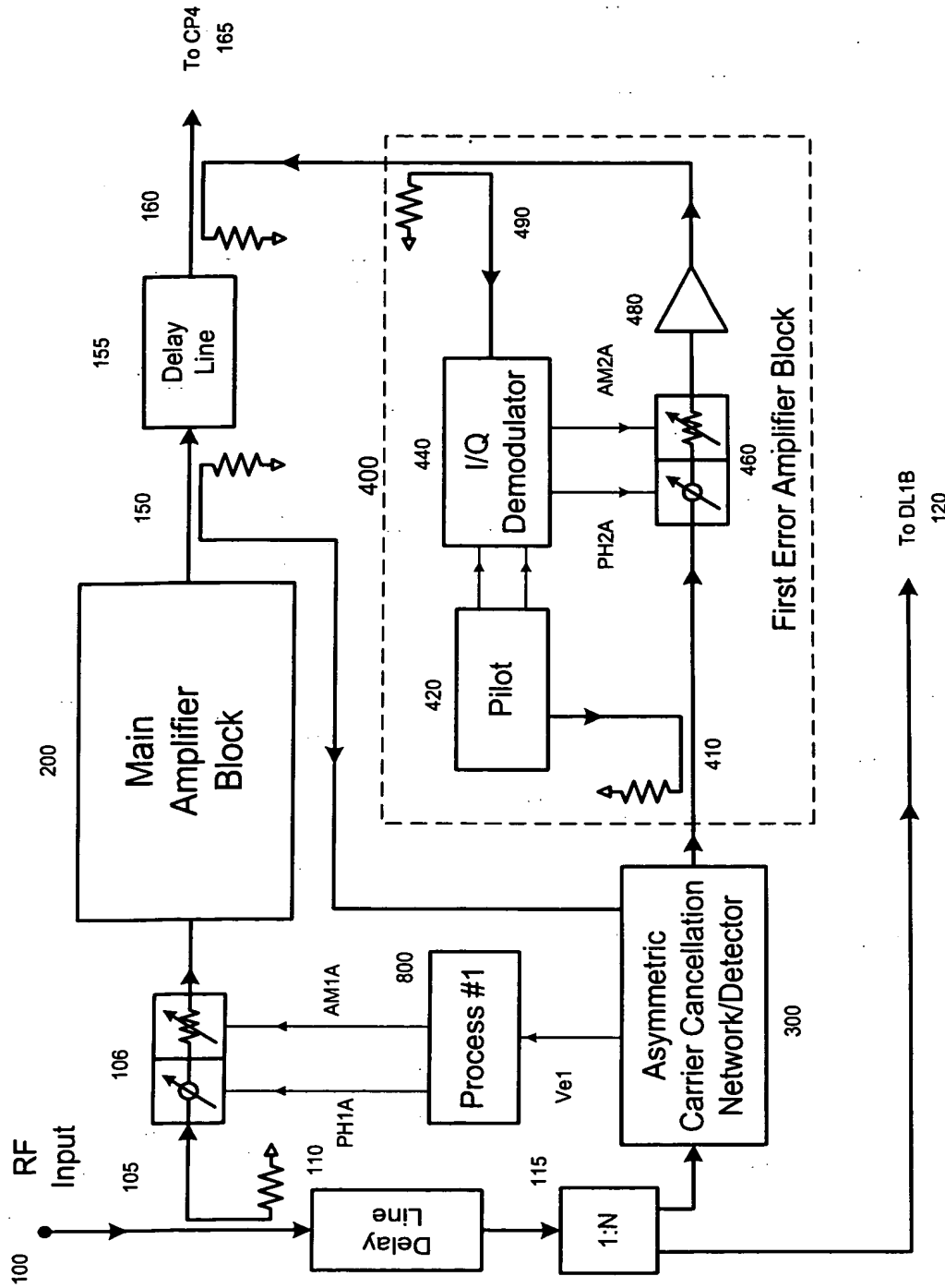


FIG. 13B

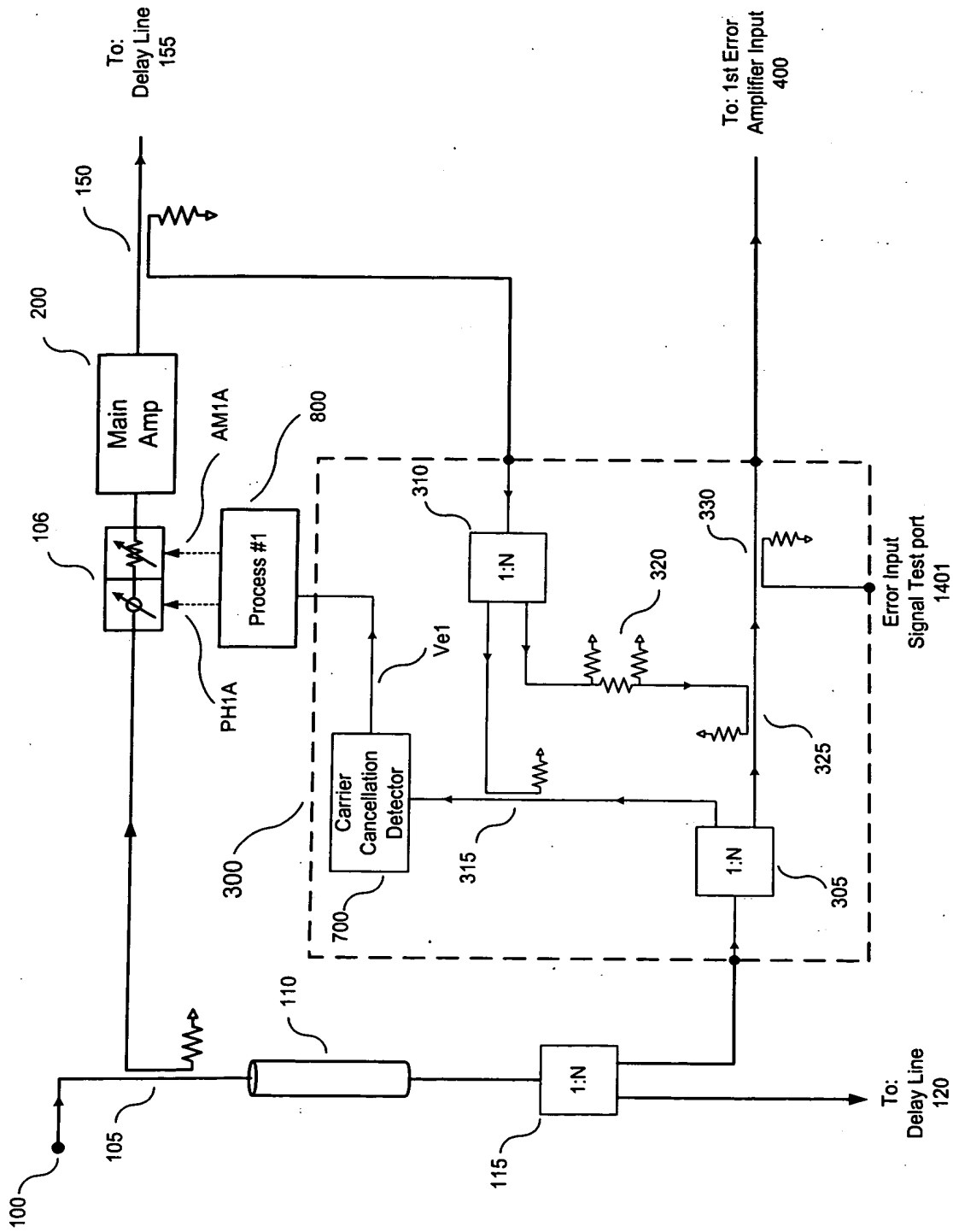


FIG. 14





FIG. 16

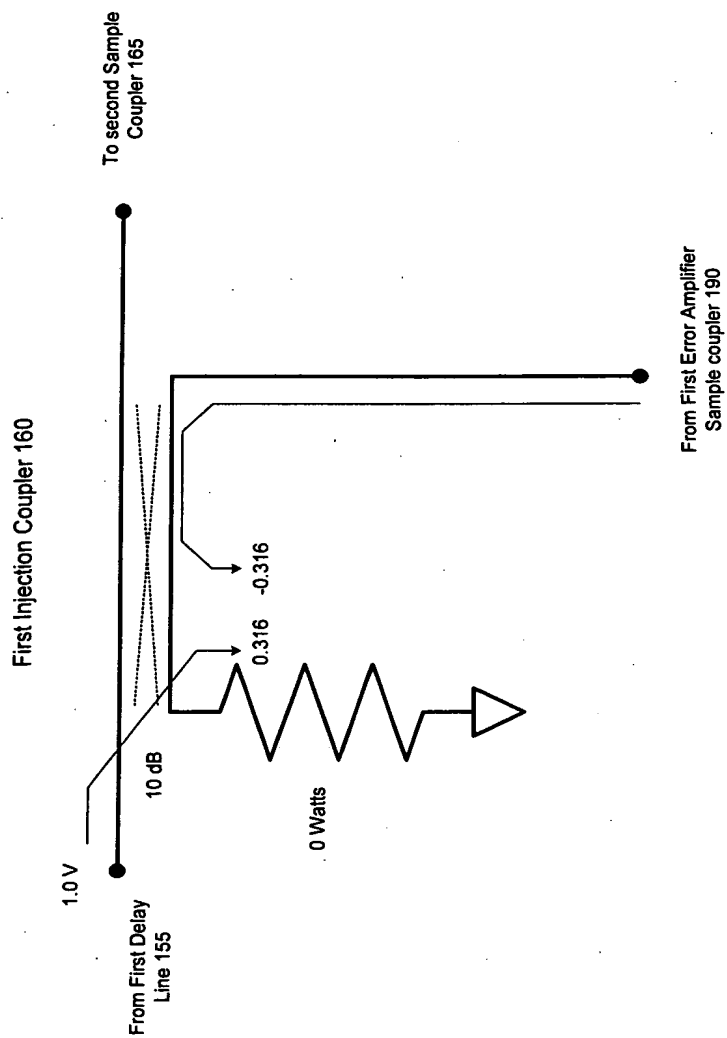


FIG. 17

Carrier Re-Injection

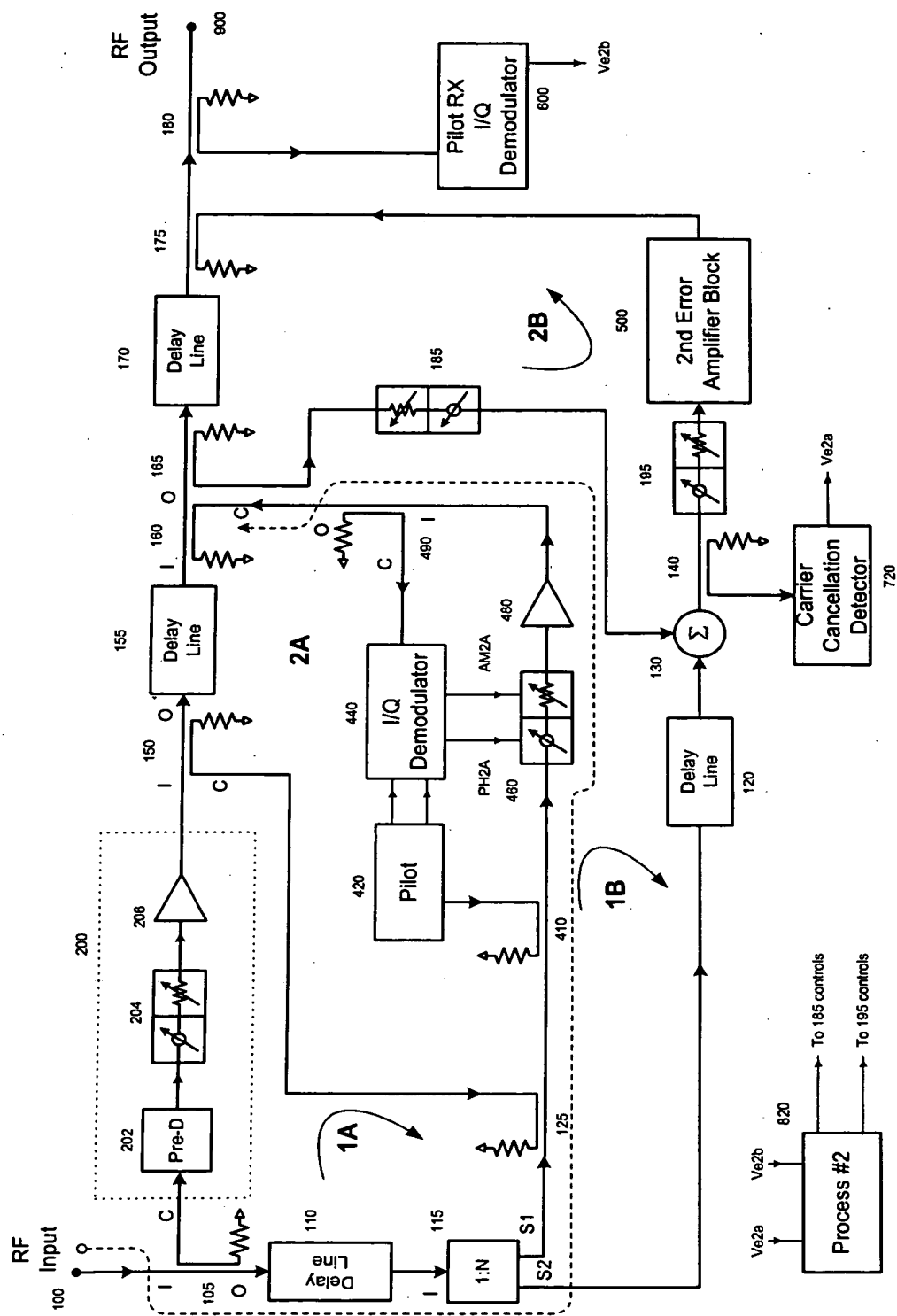


FIG. 18

